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# FACSIMILE

Date: January 29, 2002

Number of pages  
including cover sheet: 8

To:

**Examiner P. Brock II  
Group 2815, US PTO  
Washington, D.C.**

Your Ref: 09/491,302

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Our Ref: 55271US002

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Remarks: ☐ Urgent ☐ Acknowledge ☐ Reply ASAP ☐ Please Comment

Attached please find an Amendment Under 37 C.F.R. § 1.111 for the above  
reference application.

Patent  
Docket No.: 55271US002

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Application of:

John D. Geissinger, Paul M. Harvey &  
Robert R. Kieschke

Serial No.: 09/491,302

Filed: January 25, 2000

For: ELECTRONIC PACKAGE WITH  
INTEGRATED CAPACITOR

Group Art Unit: 2815

Examiner: P. Brock II

11/A

1-31-02

T. Flower

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Shawna J. Collins

**AMENDMENT UNDER 37 C.F.R. § 1.111**Commissioner for Patents  
Washington, DC 20231

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Dear Sir:

TECHNOLOGY CENTER 2800

This is in response to the office action of October 30, 2001. Please enter the amendments and consider the remarks below.

It is believed that no fee is due with this amendment. However, if this is incorrect, please charge Deposit Account No. 13-3723 any amounts due and payable by reason of this amendment.

**IN THE CLAIMS:**

Claim 1 (Amended). An electronic package, comprising:

a conductive trace layer having a first side and a second side, the conductive trace layer being patterned to define a plurality of interconnect pads;

a dielectric substrate mounted on the first side of the conductive trace layer;

a capacitor having a capacitance of from about 1 nF/sq.cm. to about 100

nF/sq.cm. including a first conductive layer, a second conductive layer and a layer of dielectric material disposed between the first and the second conductive layers, the

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